



Title: "A Branch Target Buffer (BTB) including a Speculative BTB (SBTB) and an Architectural BTB (ABTB)"

Inventor: Kiran A. Padwekar Application No.: 09/608,852 Docket No.: 42390P5563

Blakely, Sokoloff, Taylor & Zafman 303-740-1980

1/7

Fetch	Decode	Dispatch	Execute	Retire
<u>105</u>	<u>110</u>	<u>115</u>	<u>120</u>	<u>125</u>

100

FIG. 1
PRIOR ART

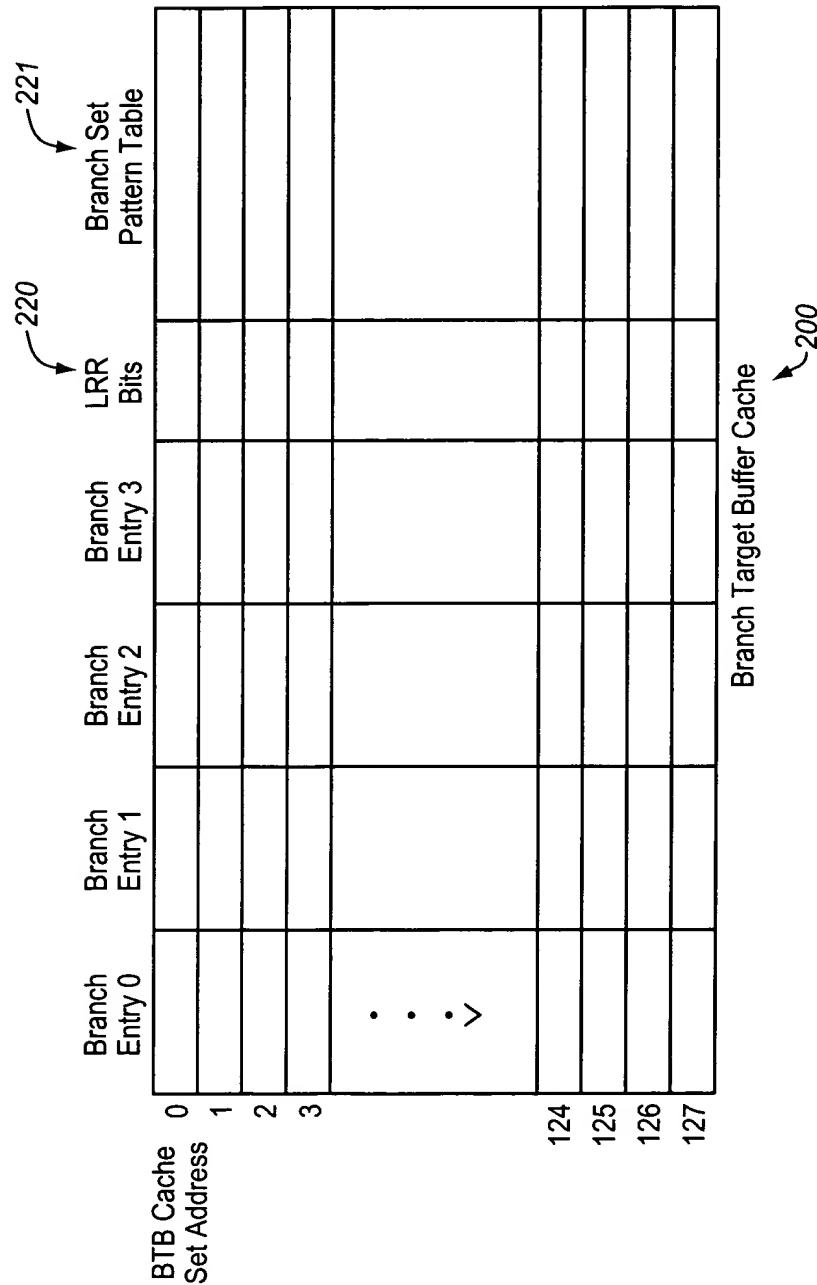


FIG. 2
PRIOR ART

3/7

Tag Field <u>310</u>	Block Offset <u>320</u>	Branch Type <u>330</u>	True History <u>340</u>	Speculative History <u>350</u>	History Selection Bit <u>370</u>	Valid Bit <u>380</u>	Branch Target Address <u>390</u>
-------------------------	-------------------------------	------------------------------	-------------------------------	--------------------------------------	---	----------------------------	---

FIG. 3
PRIOR ART

4/7

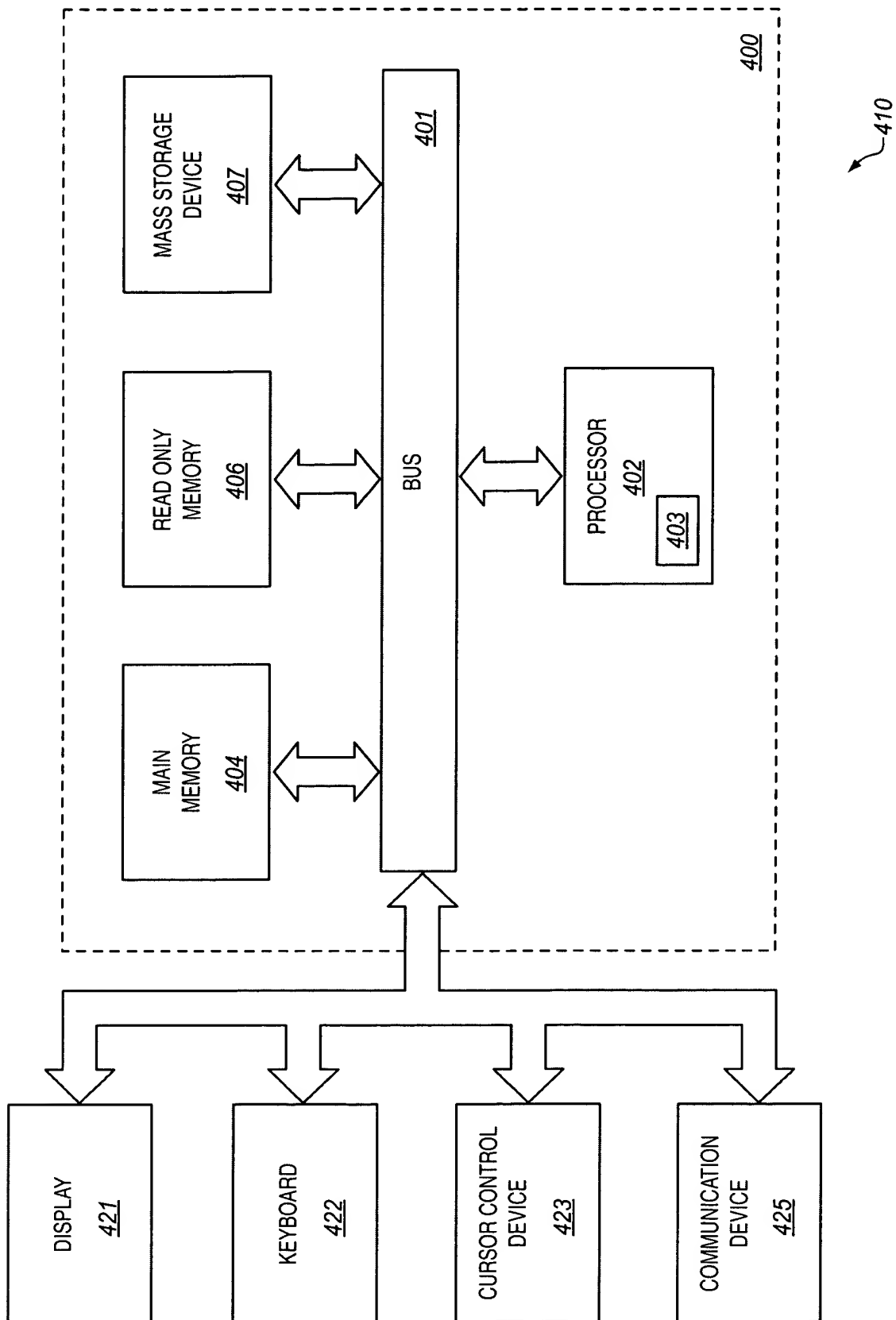
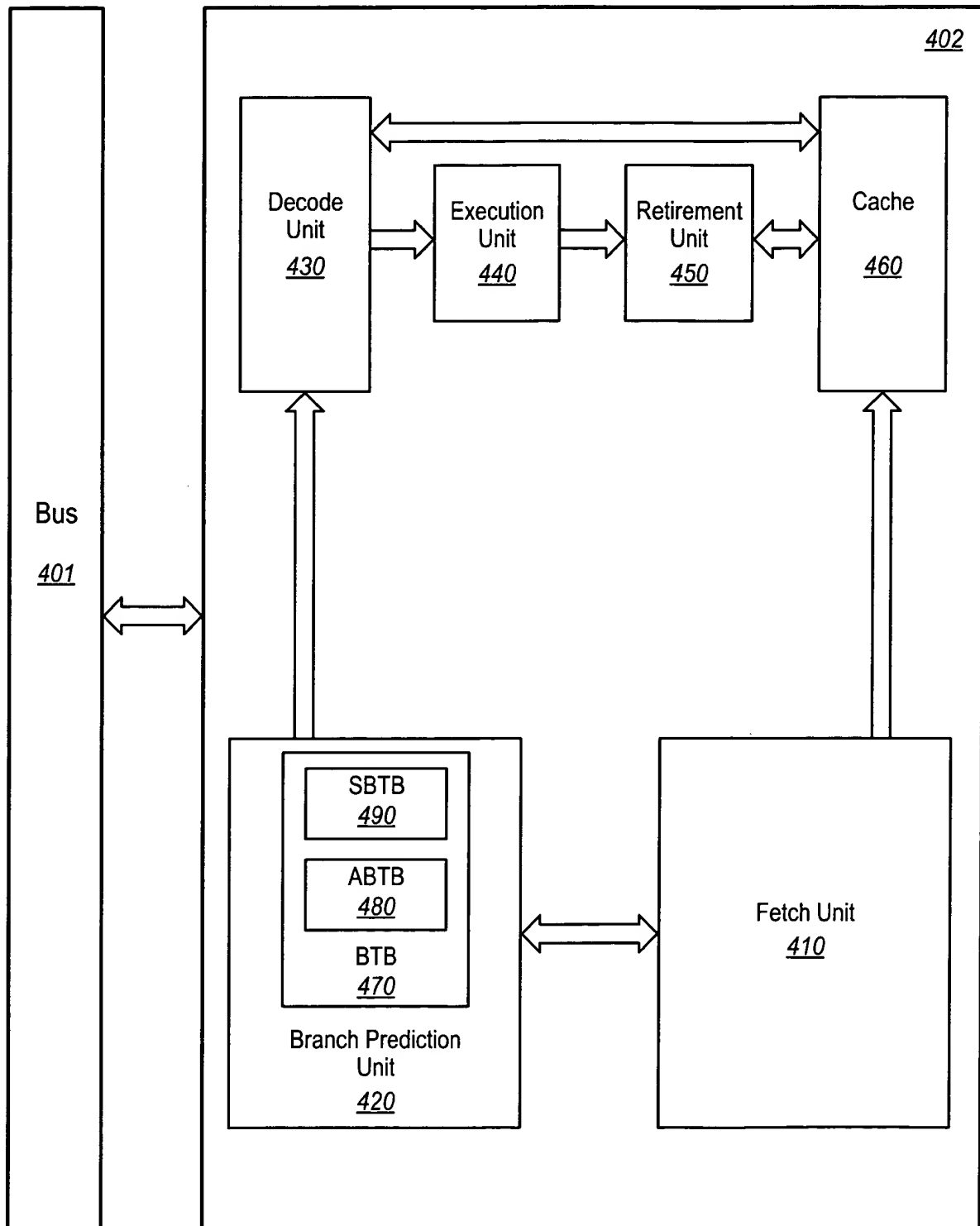


FIG. 4A

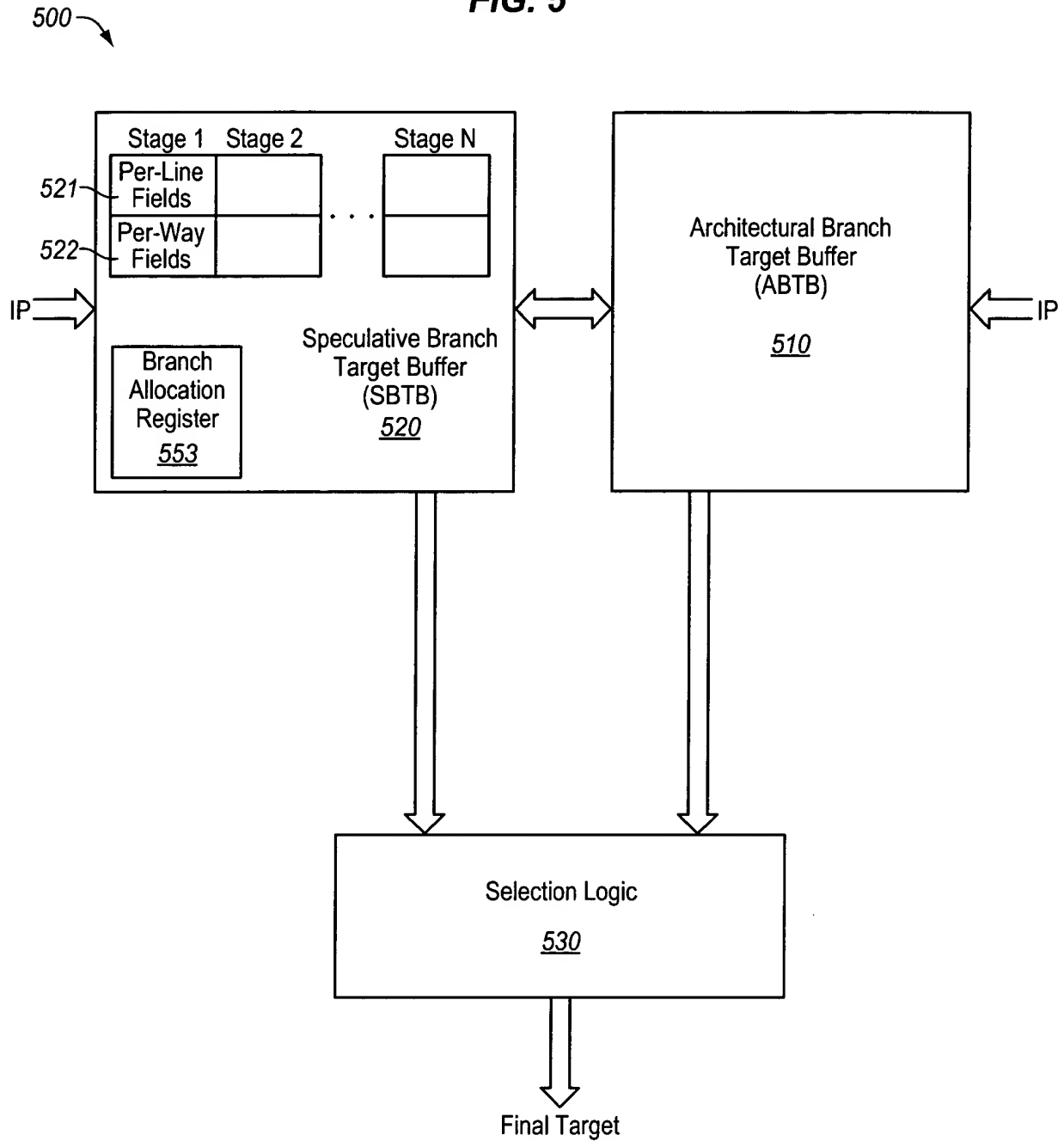
5/7

FIG. 4B



6/7

FIG. 5



7/7

FIG. 6

